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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	Unknown 10/724,796
		Filing Date	Herewith 12/01/2003
		First Named Inventor	Gerald B. Johnson
		Art Unit	Unknown 2618
		Examiner Name	Unknown Q. Vuong
Sheet 1	of 2	Attorney Docket Number	72255/00018

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
QV		HFA3861B - Schematic Diagram - Typical Transceiver Application circuit Using the HFA3861B.	
QV		HFA3861B, Intersil Data Sheet, "Direct Sequence Spread Spectrum Baseband Processor", January 2000, File Number 4816.	
QV		HFA3783, Intersil Data Sheet, "IIQ Modulator/Demodulator and Synthesizer," November 2000, File Number 4633.3	
QV		HFA3683A, Intersil Data Sheet, "2.4GHz RFIF Converter and Synthesizer," September 2000, File Number 4634.3.	
QV		HFA3983, Intersil Data Sheet, "2.4GHz Power Amplifier and Detector," March 2000, File Number 4634.4.	
QV		Harris Semiconductor, HFA3726, "400MHz Quadrature IF Modulator/Demodulator," June 1997.	
QV		Harris Semiconductor, HFA3925, "2.4GHz - 2.5GHz 250mW Power Amplifier," March 1996.	
QV		Harris Semiconductor, HFA3424, "2.4GHz - 2.5GHz Low Noise Amplifier," March 1996.	
QV		Harris Semiconductor, HFA3524, "2.5GHz/600MHz Dual Frequency Synthesizer," March 1996.	
QV		Harris Semiconductor, HFA3627, 2.4GHz RF to IF Converter," March 1996.	

Examiner Signature	/Quochien Vuong/	Date Considered	07/08/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Sheet 2

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QV		HFA3860A - Schematic Diagram, Typical Transceiver Application circuit Using the HFA3860A.	

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